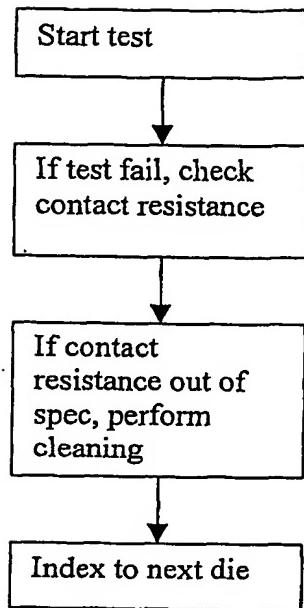
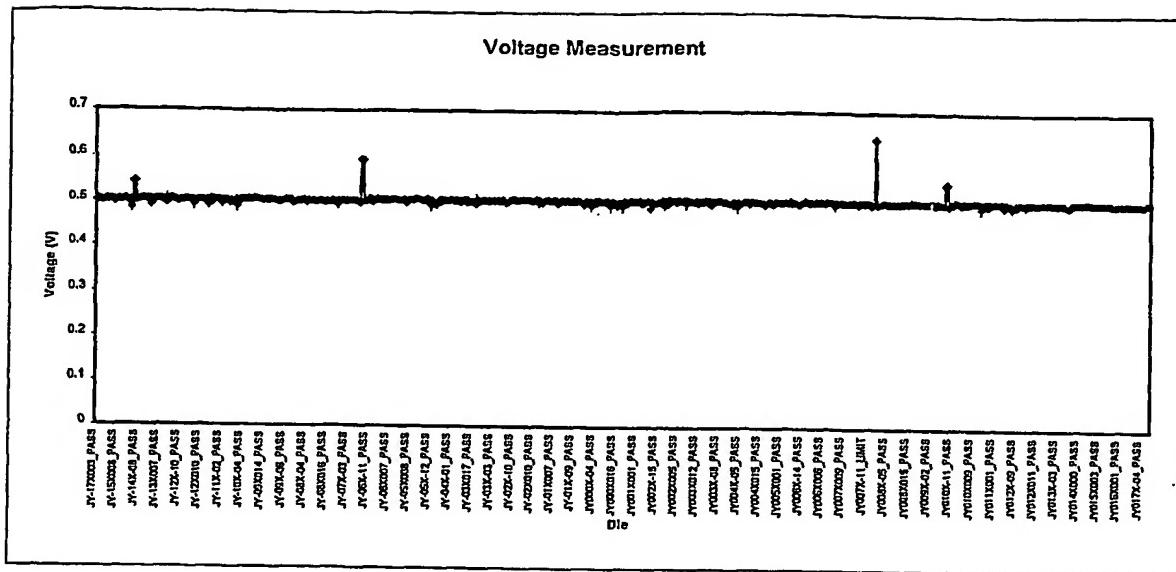


**Figure 1**



**Figure 2**



**Figure 3**

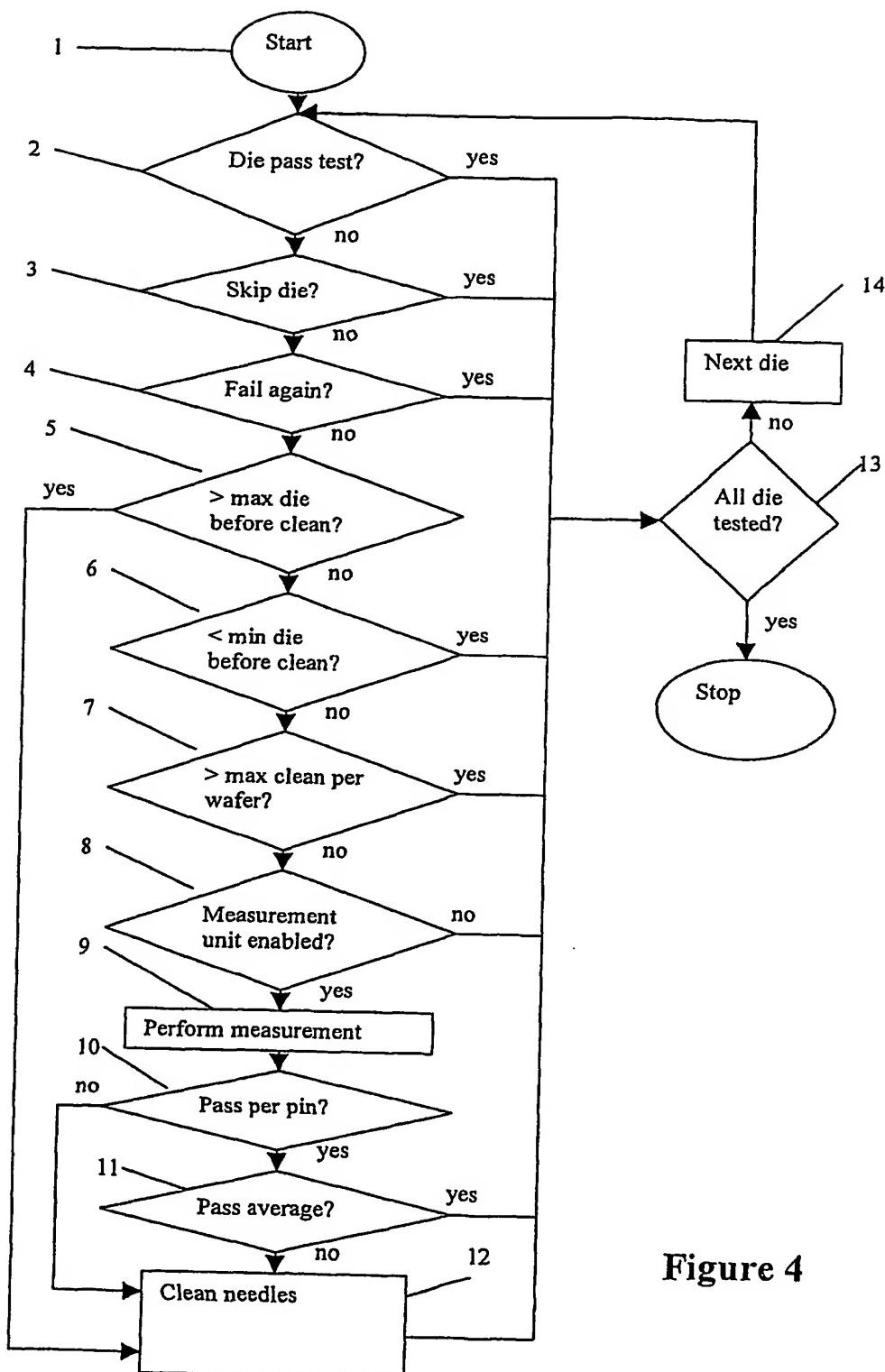


Figure 4

s16388.1\_06.20021001011407

Top			
Device :	TMF1051vr1	Tester :	D342X
Lot ID :	s16388.1_06	Touchdown :	1072
Wafer ID :	06	PMU Measure :	5
Operator ID :	1058	Indexing :	987
Timestamp :	20021001011407	Cleaning :	2
		Good Die :	898
		Recovery :	3
		RRR :	0.33
Bin to Bin Recovery		Count	%
IDD_FAIL(7) -> SCAN_SP_FAIL(16)	1	0.11	
BIST_NOM_FAIL(8) -> GOOD(1)	1	0.11	
OPEN_FAIL(2) -> GOOD(1)	1	0.11	
AMCLK(19) -> GOOD(1)	1	0.11	
FAIL -> GOOD	3	0.34	
Top 10 CRC Fail Pins		Count	%
xo(73)	2	40.00	
pll0(11)	2	40.00	
gdk1(2)	2	40.00	
gclk(1)	2	40.00	

Figure 5